

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP (303) 740-1980

Title: A Mechanism for Efficiently Supporting the Full MESI (Modified, Exclusive, Shared, Invalid) Protocol in a Cache Coherent Multi-Node Shared Memory System

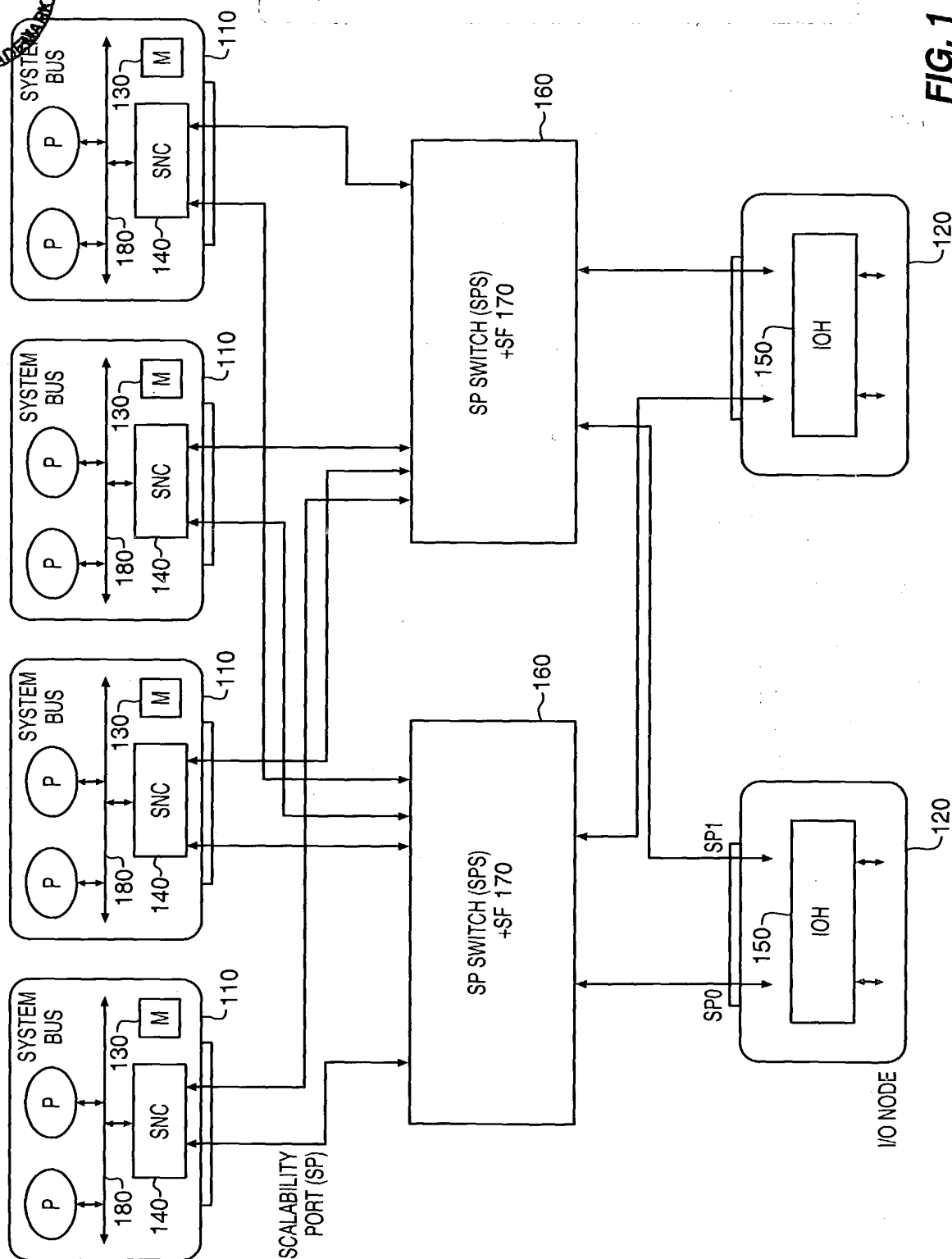
Inventor: Manoj Khare, et al.

Filing Date: December 29, 2000

Serial No.: 09/752,534

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Attorney Docket No: 042390P9878





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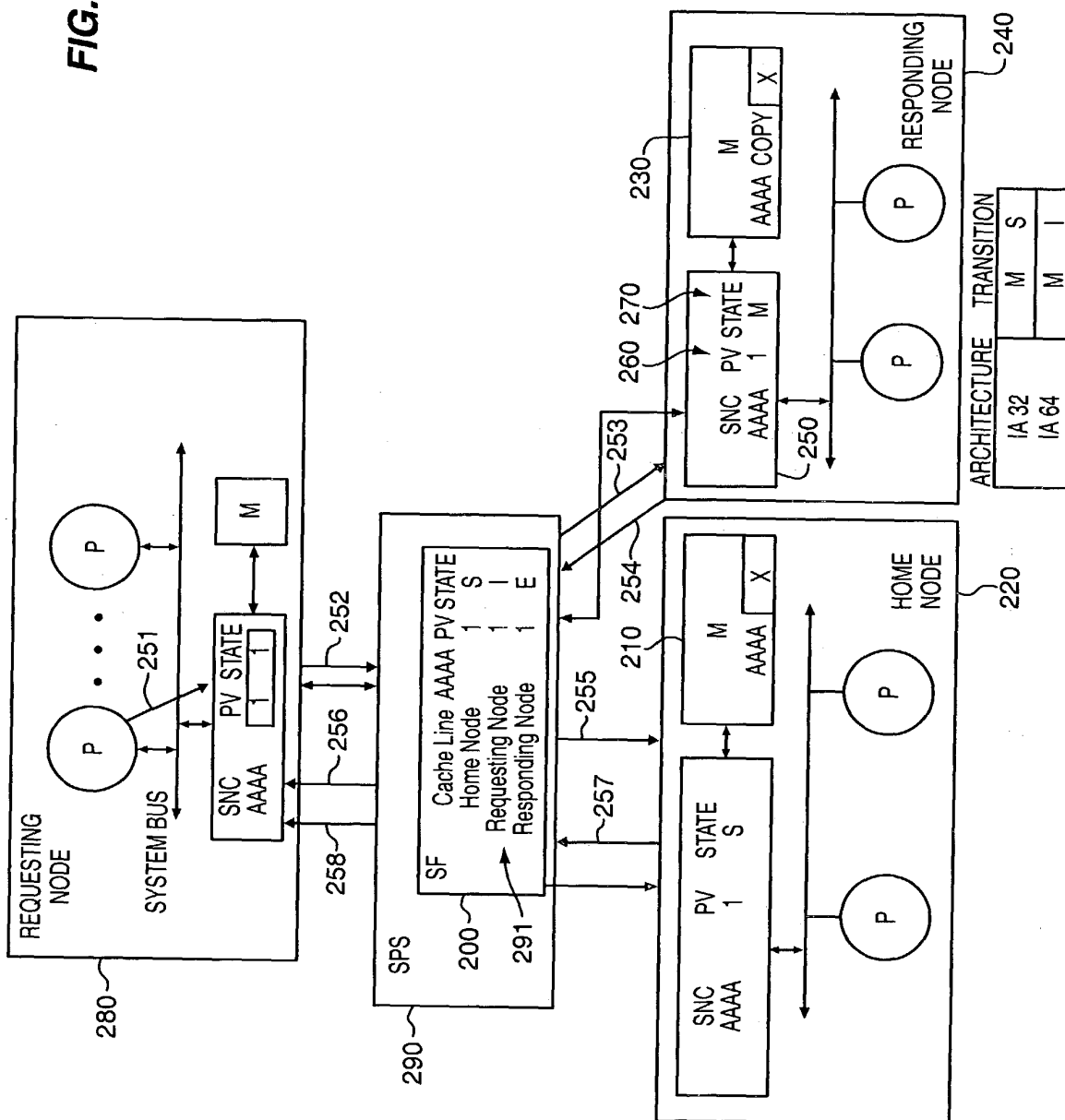
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FIG. 2



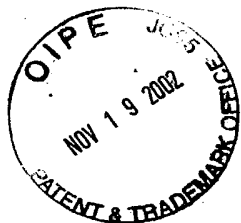
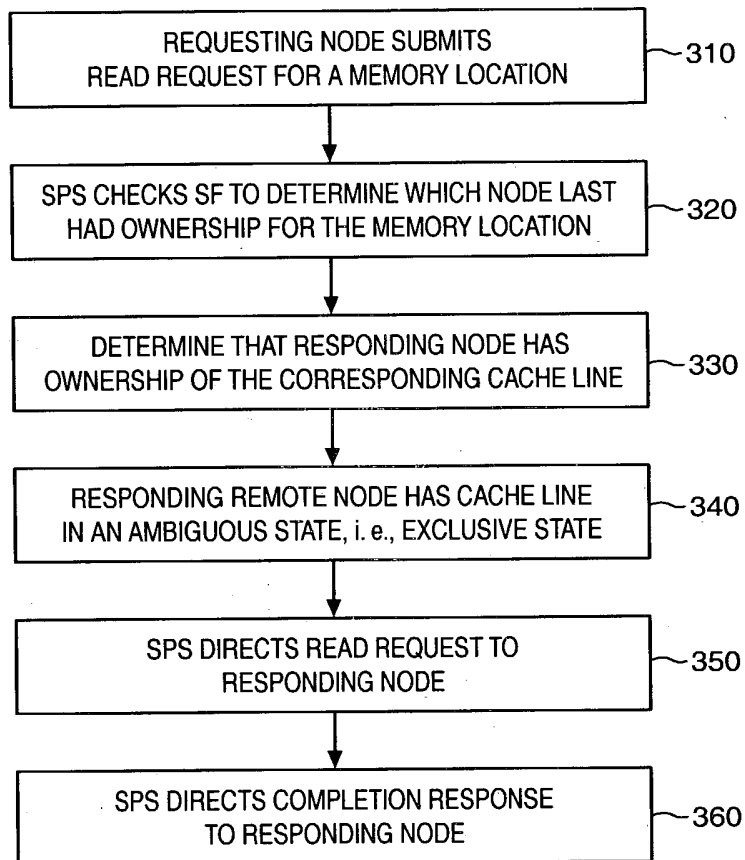


FIG. 3





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FIG. 4

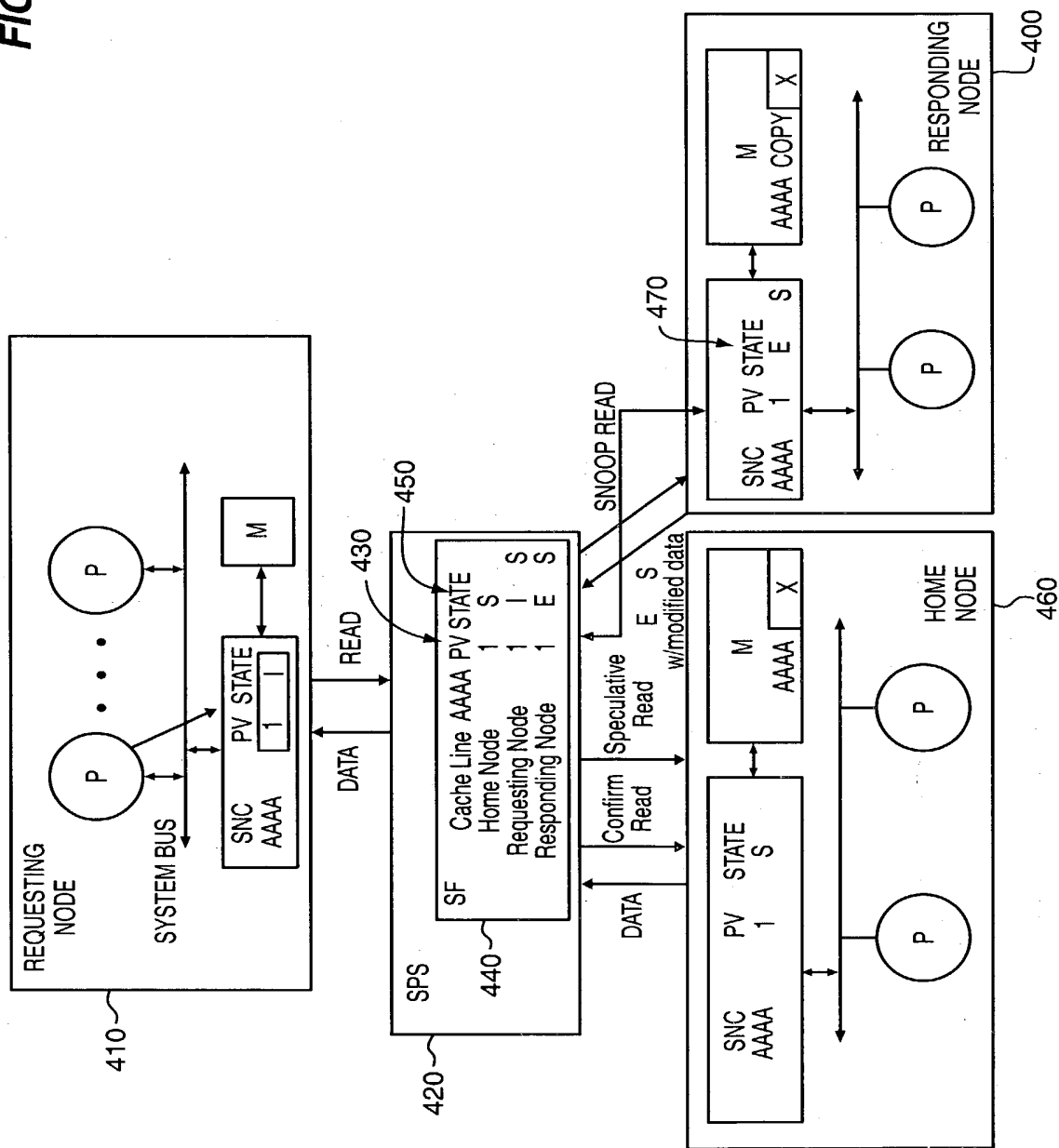




FIG. 5

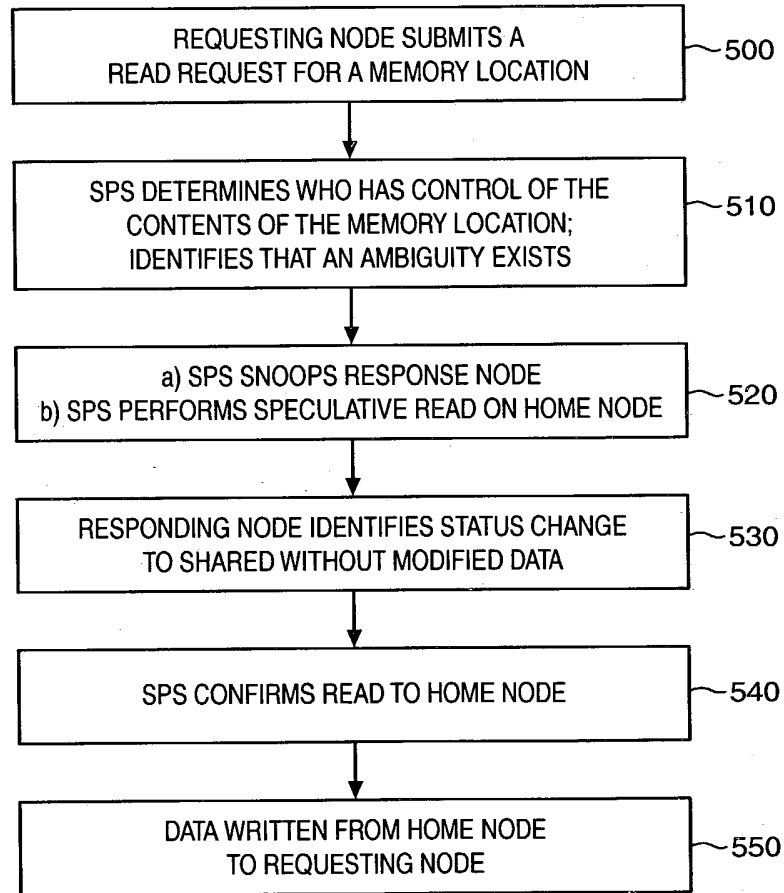
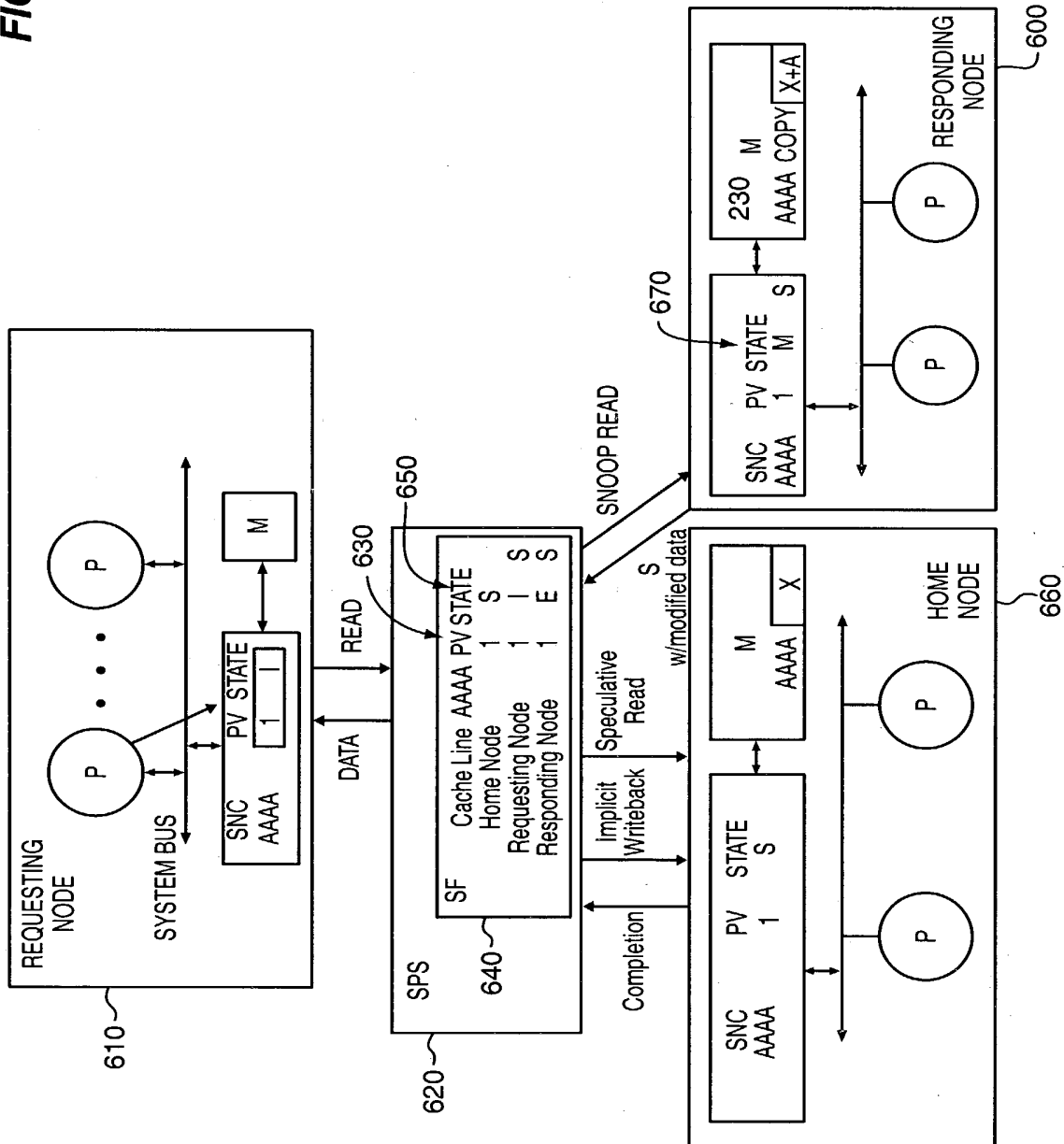
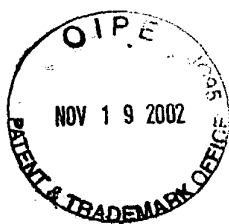




FIG. 6





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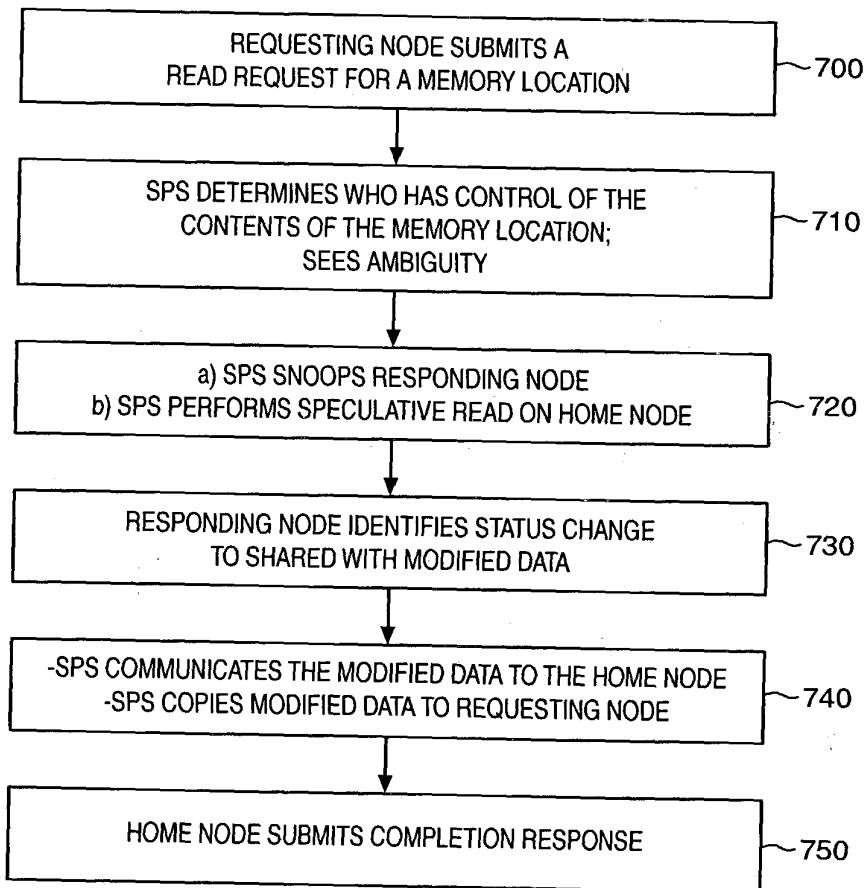
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FIG. 7



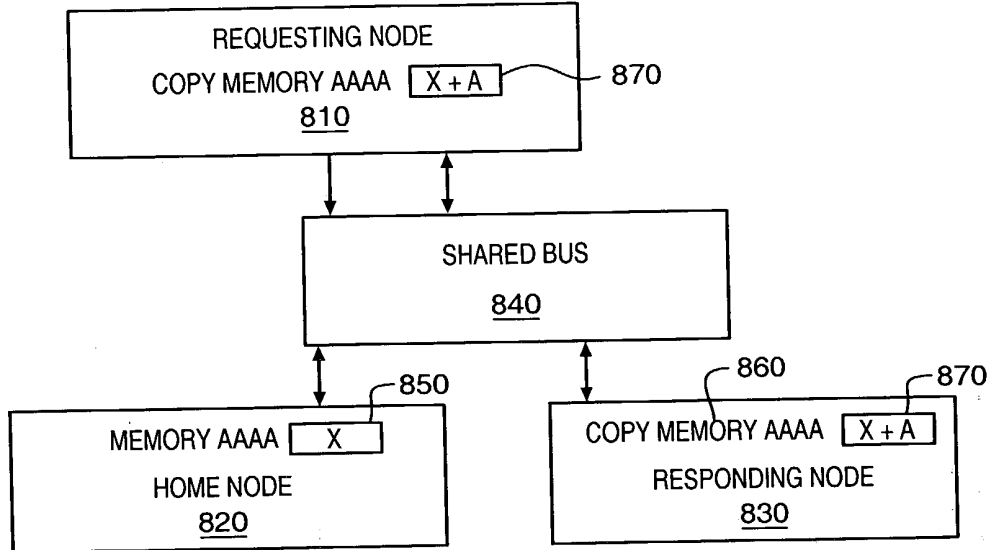


FIG. 8
PRIOR ART



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FIG. 9
PRIOR ART

